

Allegro Sigrity Si Cadence

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PowerDC (DC Power Integrity) technology Cadence Sigrity Power SI® (AC Power Integrity) technology Cadence Sigrity power-aware SI ...

Cadence and Spreadtrum Collaborate on Virtual Reference Design Kit to Reduce Customers' Design Cycle by Up to 12 Weeks

Presto is adopting Cadence Allegro® X Package Designer Plus, Clarity 3D Solver, Sigrity XtractIM technology and Celsius Thermal Solver to design IC packaging solutions.

Presto Engineering and Cadence Partner to Expand Semiconductor Package Design Solutions for Automotive and IoT Markets

The Cadence® Allegro® X Design Platform is the ... signal and power integrity (SI/PI), and logical/physical implementation. The Allegro X platform's simplified user interaction model delivers quick ...

New Cadence Allegro X Design Platform Revolutionizes System Design

Presto is adopting the Cadence system design and analysis portfolio for advanced IC packaging, which includes the Allegro X Package Designer Plus, Clarity 3D Solver, Sigrity XtractIM technology and ...

Cadence and Presto Engineering collaborate on IC packaging

integration for heterogeneous technology development across the industry-leading Cadence® Virtuoso® design platform as well as the Allegro® PCB and IC package design platforms. The V16 ...

Cadence Advances RF Design with the Latest AWR Design Environment Platform

Presto, which also provides semiconductor services such as test and qualification, will use Cadence's EDA and analysis tools (Allegro X Package Designer Plus, Clarity 3D Solver, Sigrity XtractIM ...

Week In Review: Auto, Security, Pervasive Computing

Foundries and packaging houses are wrestling how to control heat in the testing phase, particularly as devices continue to shrink and as thermally sensitive analog circuits are added into SoCs and ...

Susan Rambo

The Cadence® Allegro® X Design Platform is the ... signal and power integrity (SI/PI), and logical/physical implementation. The Allegro X platform's simplified user interaction model delivers quick ...

New Cadence Allegro X Design Platform Revolutionizes System Design

Presto Engineering, an ASIC design and outsourced operations provider, and Cadence Design Systems, Inc. (Nasdaq: CDNS) today announced a collaboration to broaden semiconductor package design solutions ...

Presto Engineering and Cadence Collaborate on IC Packaging for Automotive and IoT Markets

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